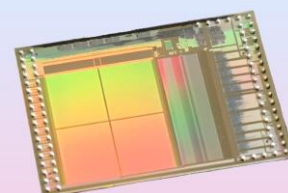




# XTOF-102-B



## High speed TOF imager chip

The XTOF-102-B chip is a general-purpose, monolithic, fully integrated photoelectric CMOS device for optical distance measurements and object detection. Its working principle is based on 3D TOF measurement. It is a system-on-chip (SOC) device and contains:

- A full data acquisition path including the modulation driver for LEDs or Laser Diodes, the photo-receiver with an 320 x 240 pixel TOF/CCD array, signal conditioning, A/D converter and signal processing.
- An on-chip controller managing data acquisition and data communication.
- An I2C interface for command and a MIPI for data communication.
- A supply-voltage power management unit.

By adding an MCU, an illumination systems and few external components, a powerful TOF camera with distance ranges of more than 200 m can be built.

The working principle is based on the time-of-flight (TOF) of photons emitted by the illumination and reflected back by the object to the photosensitive pixels. It measures the phase-shift between the emitted and received signal which is proportional to the distance. A very high photo-sensitivity and high resolution ADC allows accuracy down to a centimeter depending on the lens, the illumination power and the modulation frequency.

Due to the unique CCD/CMOS technology, the TOF chip performance is just shot noise limited.

## APPLICATIONS

- Volumetric Sensing
- High Frame Rate Applications
- Robotics and Drone Navigation
- Smart Home Automation Sensors

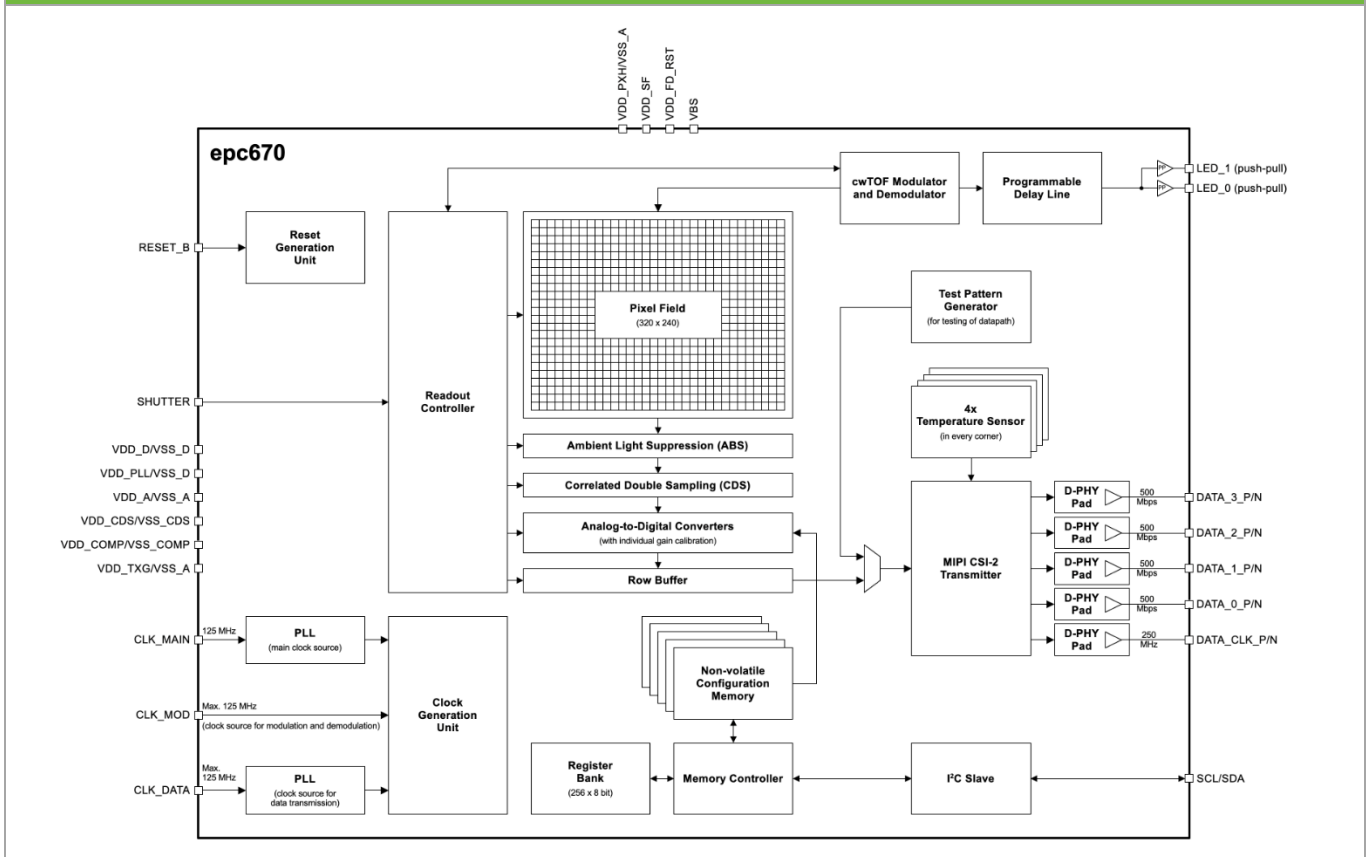
## YOUR BENEFITS

- Operating range of up to 240 m
- Very small footprint of 11.7 x 9.3 mm
- Up to 700 TOF frames per second
- Full ambient light tolerant (0 .. 100 kLux) with active ambient light suppression

## SPECIFICATIONS

- 320 x 240 pixel, pixel pitch 20  $\mu$ m
- Quantum efficiency 85 % @ 850 nm
- Power consumption 1,250 mW
- Integrated illumination driver
- Low distance noise of  $\leq 10$  mm @25% signal
- Output data with 12 bit resolution
- Pixel binning supported

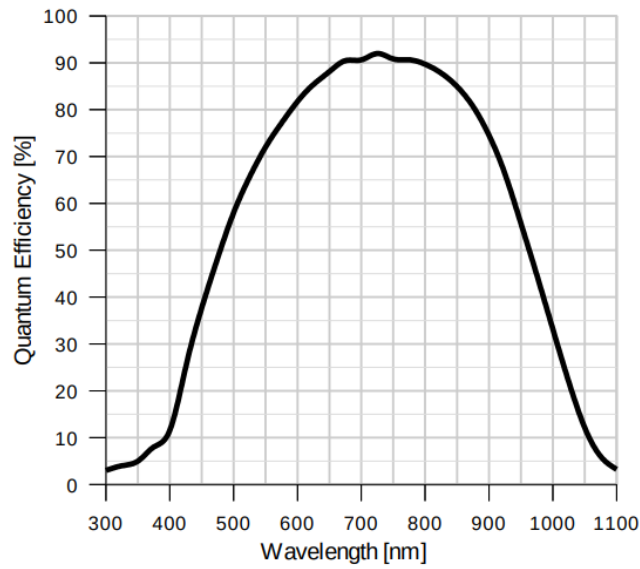
**FIG 1. BLOCK DIAGRAM**



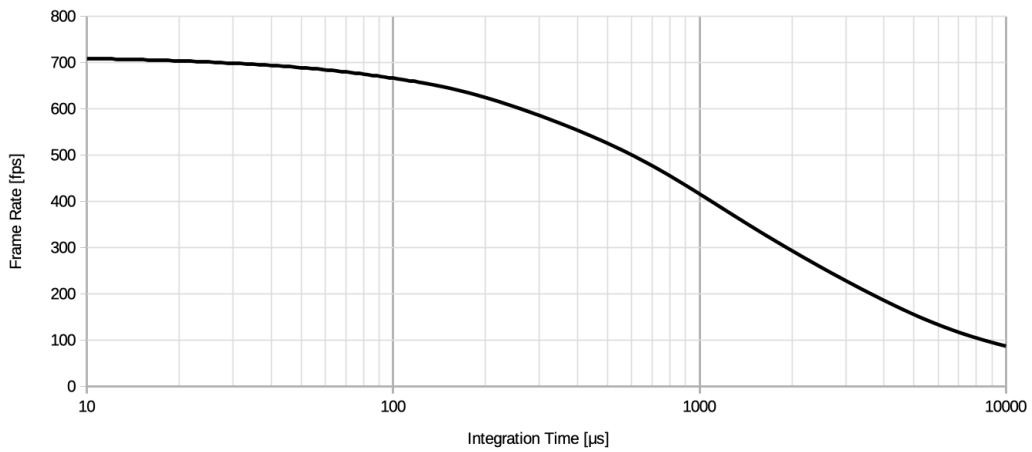
## Key characteristics

Parameter	Conditions	Symbol	Min	Typ	Max	Units
Supply Voltage		VDD	1.71	1.8	1.98	V
		VDDA	4.9	5.0	5.1	
		VDDPIX	9.5	10.0	10.5	
		VBS	-20.0	-15.0	-14.5	
Power consumption	300 fps / $T_{int} = 500 \mu s$	P		1,250		mW
Illumination Driver		$I_{LED}$			200	mA
Pixel Pitch				20		$\mu m$
Optical fill factor				100		%
TOF Sensitivity		$S_{TOF}$		8		e-/LSB
Grayscale Sensitivity	$\lambda = 850 \text{ nm}$ , $T_{int} = 100 \mu s$	$S_{Gr}$	0.19	0.25	0.31	nW/mm <sup>2</sup> /LSB
Optical Sensitivity	$T_{int} = 100 \mu s$	$H_v$		150k		LSB/Lux/s
Ambient light suppression	$\lambda = 940 \text{ nm}$ , $T_{int} = 100 \mu s$	$E_b$		60		$\mu W/mm^2$
Quantum Efficiency	$\lambda = 850 \text{ nm}$	QE		85		%
Wavelength range		$\lambda$	400		1030	nm
Modulation frequency	Internal/External	$f_{Mod}$	0.63		31.25	MHz
Data communication	MIPI CSI-2 v2.1, 4 lanes				500	Mbps per lane
Programmable Delay Line	In 2.1 ns steps	$t_{del}$	0		103	ns
3D TOF Pixel Rate		$f_{Pix}$			54	MPix/s
Temperature Range	Operating	$T_{Op}$	-40		105	$^{\circ}C$

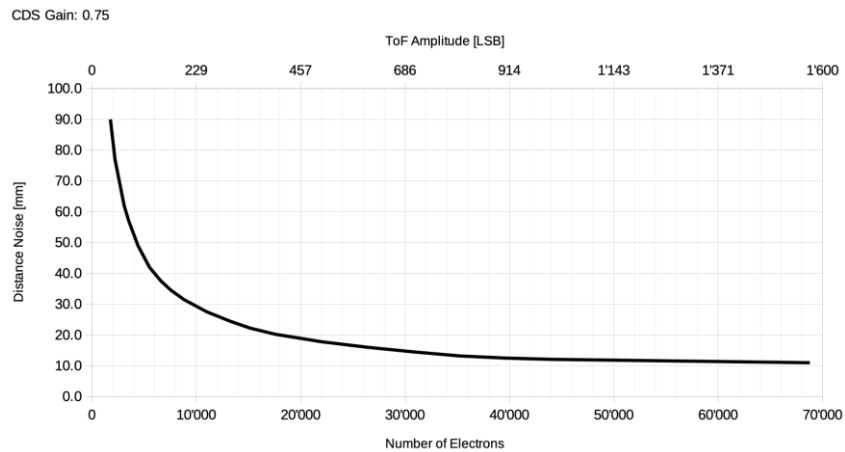
**FIG 2. QUANTUM EFFICIENCY**



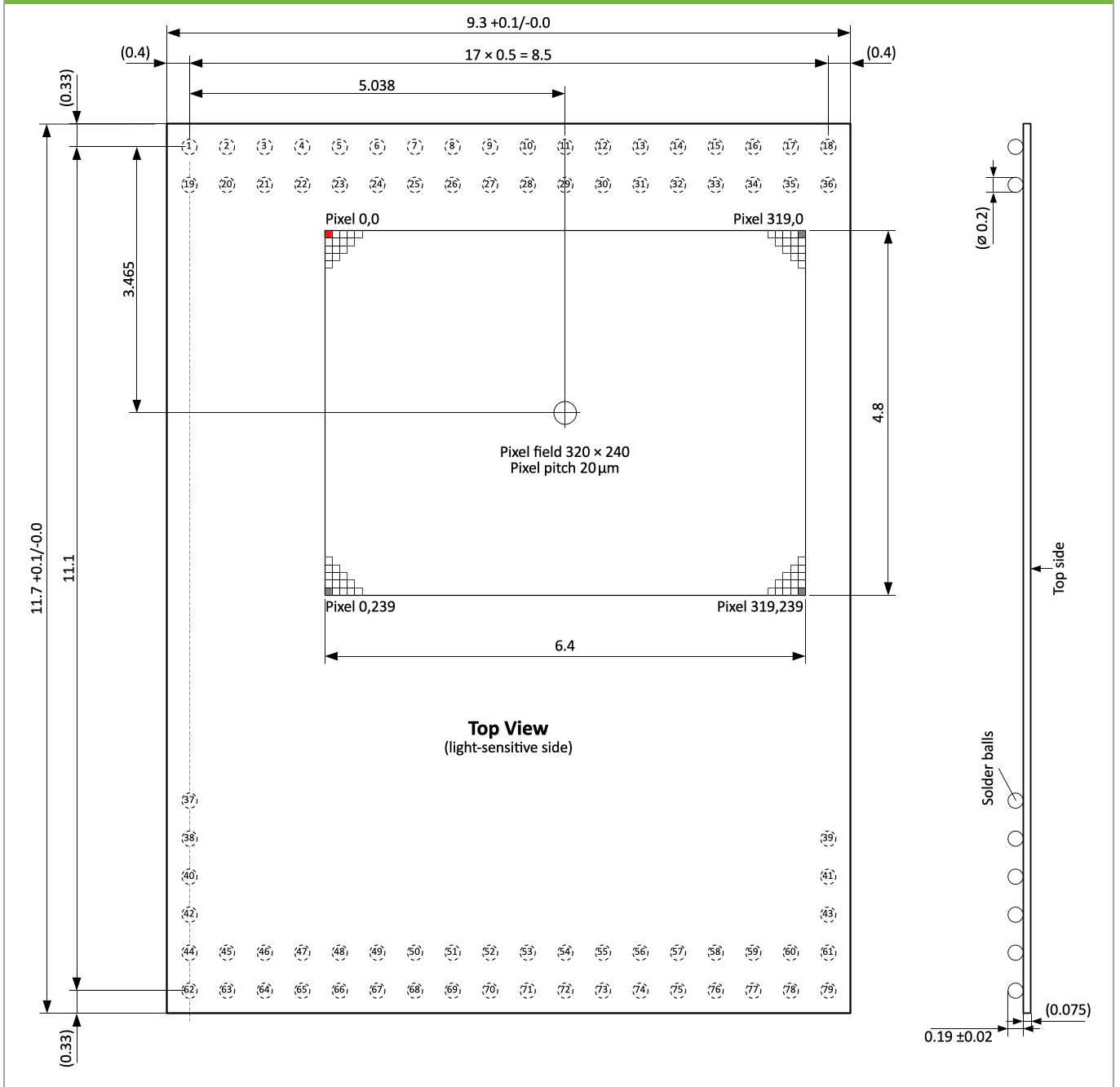
**FIG 3. ACHIEVABLE DCS FRAME RATE BASED ON INTEGRATION TIME**



**FIG 4. DISTANCE NOISE VS SIGNAL LEVEL**



**FIG 5. MECHANICAL DIMENSIONS**



## Testing and operation methods

Excelitas verifies the electro optical specifications on every device. Electrical and imaging performance tests as well as visual inspection (AOI) during fabrication is performed as per our quality standard. Failed dies are removed.

Excelitas Technologies is certified to meet ISO-9001.

## Packaging and shipping

The devices are delivered on JEDEC tray for 20 x 7 pieces, maximum quantity 140 pieces per tray. Further tray specifications can be found in the JEDEC Association standard JEP95.

## Storage and handling

Excelitas highly recommends following the notes below:

- Keep devices in an ESD controlled environment until final assembly.
- Open the sealed packing just before SMT assembly. Once the sealed packing is open, keep the devices under N2 atmosphere to avoid corrosion and oxidation of the solder ball surface.
- PCB design and SMD manufacturing process shall be according to our Chip-Scale Package Assembly Process Guidelines.

## RoHS compliance

This series of TOF chips is designed and built to be fully compliant with the European Union Directive on restrictions on the use of certain hazardous substances in electrical and electronic equipment.



## Warranty

A standard 12-month warranty following shipment applies.





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